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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/829,317	04/21/2004	Wen-Kuen Chen	E0523-00059	5674
8933	7590	04/10/2007	EXAMINER	
DUANE MORRIS, LLP			DINH, TRINH VO	
IP DEPARTMENT				
30 SOUTH 17TH STREET			ART UNIT	
PHILADELPHIA, PA 19103-4196			PAPER NUMBER	
			2821	
SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE		
3 MONTHS	04/10/2007	PAPER		

**Please find below and/or attached an Office communication concerning this application or proceeding.**

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

**Office Action Summary**

Application No.

10/829,317

Applicant(s)

CHEN ET AL.

Examiner

Trinh Vo Dinh

Art Unit

2821

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 19 March 2007.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 5,9,11 and 14 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 5,9,11 and 14 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                       | 5) <input type="checkbox"/> Notice of Informal Patent Application                       |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

### DETAILED ACTION

This is a response to an RCE filed 03/19/2007. Claims 5, 9, 11 and 14 are pending in the application.

#### *Claim Rejections - 35 USC § 103*

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 5, 9 and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yoshimura (JP 2000-058260 of record) in view of Wang (US 6,043,478 of record).

Respecting claim 5, Yoshimura discloses, in Figs. 1-2 and abstract, organic light-emitting diode structures forming an array, each of the organic diode structures comprising first and second anodes (1, 1), first and second organic light emitting layers (2,2) disposed between the first and second anodes, and a common electrode (3) disposed between the first and second organic light-emitting layers (2, 2), wherein the first light-emitting layer is for substantially emitting light in a first direction and the second organic light-emitting layer is for substantially emitting light in a second direction opposite to the first direction (abstract). However, Yoshimura does not suggest transistors coupled to diode structures. Wang discloses, in Fig. 4, a first transistor (M1) coupled to each of the organic light-emitting diode structures (D1, D2), the first transistor (M1) coupled to one of the first and the second anodes of the OLED structures (D1), a second transistor (M2) coupled to each of the organic light-emitting diode structures (D1, D2), the second transistor (M2) coupled to the other one of the first and the second anodes of the

Art Unit: 2821

OLED structures (D2), Wang further discloses a third transistor (M4, M3) coupled to the first and the second transistors (M1, M2), the third transistor (M3, M4) switching the first and second transistors (M1, M2). It would have been obvious to one having ordinary skill in the art at the time the invention was made to provide Yoshimura 's display with transistors' configuration as taught by Wang in order to reduce the process complexity (Wang: col. 2 lines 6-13).

Respecting claim 9, Yang discloses the first and second transistors (M1, M2) driving the OLED structures (D2, D2).

Respecting claim 11, Yoshimura discloses light being emitted from at least one of the first and second organic light-layers (2, 2) when an electric current is passed between one of the first and the second anodes (1, 1) and the common electrode (3).

3. Claim 14 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kontogeorgakis et al (US 6,822,611 of record) in view of Yoshimura as modified by Wang.

Respecting claim 14, Kontogeorgakis discloses, in Fig. 4, a telecommunication device comprising a main body (12), a flip-up door (14), and a display (16) beneath the flip-up door. However, Kontogeorgakis does not suggest the display comprising diode structures which each structure including anodes, organic light-emitting layers, an electrode and transistors. Yoshimura as modified by Wang discloses the display comprising the organic light-emitting diode structures as discussed above in claim 5. It would have been obvious to one having ordinary skill in the art at the time the invention was made to employ Yoshimura's OLED structures to communication device of Kontogeorgakis. Doing so would reduce display size to reduce an emission area.

*Inquiry*

Art Unit: 2821

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Trinh Vo Dinh whose telephone number is (571) 272-1821. The examiner can normally be reached on Monday to Friday from 9:30AM to 6:00PM. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Douglas Owens, can be reached on (571) 272-1662. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

*Art unit 2821*

*March 30, 2007*

TRINH DINH  
PRIMARY EXAMINER

A handwritten signature in black ink, appearing to read 'Trinh Dinh', with a long horizontal flourish extending to the right.